HPC in Cryptanalysis A short tutorial

Antoine Joux

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Historical link







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Historical link



Background activity in support of research

Historical link



Background activity in support of research

• Fun (but sometime frustrating)

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Aimed at record breaking / new algorithms benchmarking

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No real need for reusability

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- Aimed at record breaking / new algorithms benchmarking
- No real need for reusability
- Have to be performed on whatever is available

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- No real need for reusability
- Have to be performed on whatever is available
- Computations are easy to check

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Algorithmic starting point

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- Algorithmic starting point
 - Validation by toy implementation

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• Find computing power / Choose target computation

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• Program / Debug / Optimize

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• Find computing power / Choose target computation

Program / Debug / Optimize

• Run and Manage computation

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Lattice reduction and applications

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- Lattice reduction and applications
- Collisions and multicollisions

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- Collisions and multicollisions
- Elliptic curves, pairings, volcanoes

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- Decomposition algorithms (Knapsacks, codes)

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- Gröbner bases

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Stopping at toy implementations

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Stopping at toy implementations

Pairings

- Comparing the MOV and FR Reductions in E. C. Crypto Harasama, Shikata, Suzuki, Imai
 - \Rightarrow Faster implementation using Miller's technique
- Can be used constructively: Tripartite Diffie-Hellman

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Volcanoes

• Pairing the volcano, Ionica, J.

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- Old-fashioned technique: Use/buy dedicated local machines
 - Easy to arrange (assuming funding available)
 - Good control of the architecture choice
 - Control on the availability of the computing resources
 - Not easy to scale

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- Email computations: Use idle cycles on desktop
 - Total available power is potentially huge
 - No control on choice of architecture or availability
 - Very limited communication bandwidth
 - Need to deal with "adversary" ressources
 - Need for a very user-friendly client

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- Apply for power on HPC ressources
 - Very high-end dedicated computers
 - Fast communication
 - Need to use the existing architecture
 - Job management in a multi-user context is hard
 - Challenge: adapt to the massively parallel environment

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HPC in the Cloud

Choosing a target

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Choosing a target

Quality of target:

- Proof of concept only
- Real size demo
- Attack cryptographic size parameters or record

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• Reasonable feasability assurance

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Proof of concept case

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Proof of concept case

- Differential collisions in SHA-0, Chabaud, J. Full collision out of reach: Demo collisions
 - 80-rounds on partially linearized functions
 - 35-rounds on SHA-0

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Decoding random binary linear codes in 2^{n/20}.
 Becker, J., May, Meurer

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 A practical attack against knapsack based hash functions Granboulan, J. (1994)
 14 h single CPU, 25% success rate

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- Elliptic curve discrete logarithm problem over small degree extension fields J., Vitse (JoC 2011) Adapted version of GB computations

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- Main rule: avoid nasty surprises
 - Program from scratch
 - Conservative and defensive programming

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- Expect problems, hardware can fail
 - Power down risk: Need ability to restart computation
 - Availability problems: Avoid tight schedule
 - Hardware faults can damage computations
 Check intermediate data

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Size of computations — Some reference points

- DLOG GF(p) 160-digits (Kleinjung 2007): 3.5 + 14 CPU.years
- RSA-768 (Kleinjung et al. 2009): 1500 + 155 CPU.years
- RSA-200 (Bahr, Boem, Franken Kleinjung 2005): 55 + 20 CPU.years
- ECC-2K130 (Bernstein et al.): \approx 16 000 CPU.years
- 10 trillion digits of π (Yee, Kondo 2011) : 12 cores, 90 days: 3 CPU.years
- Largest project in last PRACE call (climate simulation): 16500 CPU.years

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- Main gain: Reduced memory cost

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- Published in *Collisions of SHA-0 and Reduced SHA-1*, Biham, Chen, J., Carribault, Lemuet, Jalby (2005)

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Example 3: Triple collisions (2009)

 Improved generic algorithms for 3-collisions, Lucks, J. Asiacrypt 2009

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- Improved generic algorithms for 3-collisions, Lucks, J. Asiacrypt 2009
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 - Phase 1: Compute iterations $F^i(R)$ from random R
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- Phase 3, less costly than Phase 1, harder to code Done on CPUs

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 - Phase 2: Sort by end point values
 - Phase 3: Restart from triples with same end points and recompute
- Needs raw computing power, low communication/disk
 Phase 1 on CLIDA graphics and (a) 8 times faster the
 - \Rightarrow Phase 1 on CUDA graphics card (\approx 8 times faster than the CPUs on the available machines)
- Phase 2, easy step, on single CPU
- Phase 3, less costly than Phase 1, harder to code Done on CPUs
- Triple collision on 64-bits cryptographic function Magnitude of computation : 100 CPU.days

Example 4: Index calculus

Antoine Joux HPC in Cryptanalysis

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Example 4: Index calculus

- A known landscape:
 - Discrete log. in GF(p): 90 digits (1998), 100 digits (1999), 110 digits (2001), 120 digits (2001), 130 digits (2005)
 - Discrete log. in GF(2ⁿ): 521 bits (2001), 607 bits (Thomé 2002, 2005), 613 bits (2005)
 - Discrete log. in GF(pⁿ): 65537²⁵, 120 digits (2005), 370801³⁰, 168 digits (2005)
 - When e-th roots become easier than Factoring, J., Naccache, Thomé 2007
 - Oracle assisted static DH, J., Lercier, Naccache, Thomé 2008
 - Oracle assisted static DH on Oakley curve (Granger, J., Vitse 2010)

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Example 4: Index calculus

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Not a routine task !

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Index calculus in finite fields

GF(p)	90	100	110	120	130
CPU.days	150	260	70	280	340
Computers	4 × 1 + 1	8 × 1 + 1	1 × 4	1 × 4	1 × 16

GF(2 ^{<i>n</i>})	521	607	613	
CPU.days	120	560	1100	
Computers	1 × 4	1 × 16	4 × 16	

Other	65537 ²⁵	370801 ³⁰	RSA-155 <i>e</i> -th roots
CPU.days	2	0.5	2
Computers	1	$1 \times 16 + 1 \times 8$	20

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Antoine Joux HPC in Cryptanalysis

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- Theory:
 - Phase 1: Sieving
 - Phase 2: Linear algebra
 - Phase 3: Individual logarithms

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- Theory:
 - Phase 1: Sieving
 - Phase 2: Linear algebra
 - Phase 3: Individual logarithms
- Practice:
 - Phase 1:
 - 1a: Sieving
 - 1b: Verification of relations (fast)

- Phase 2: Linear algebra
- Phase 3: Individual logarithms

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- Theory:
 - Phase 1: Sieving
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 - Phase 3: Individual logarithms
- Practice:
 - Phase 1:
 - 1a: Sieving
 - 1b: Verification of relations (fast)
 - Phase 2:
 - 2a: Structured Gaussian Elimination (fast)
 - 2b: Lanczos algorithm
 - 2c: Completing the logarithms (fast)
 - Phase 2: Linear algebra
 - Phase 3: Individual logarithms

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- Theory:
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 - Phase 3: Individual logarithms

• View confirmed by 6×22

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Computation performed on GENCI's Titane computer (Project t2010066445)

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Computation performed on GENCI's Titane computer (Project t2010066445)

• Sieving: About 1 hour on 200 CPUs



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Computation performed on GENCI's Titane computer (Project t2010066445)

- Sieving: About 1 hour on 200 CPUs
- SGE: from 50 M eq. in 2.1 M var.
 - \Rightarrow 666 K eq./var.

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Computation performed on GENCI's Titane computer (Project t2010066445)

- Sieving: About 1 hour on 200 CPUs
- SGE: from 50 M eq. in 2.1 M var.
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- Lanczos 27 hours on 128 CPUs

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- SGE: from 50 M eq. in 2.1 M var.
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- Completion, 10 min single CPU

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- Lanczos 27 hours on 128 CPUs
- Completion, 10 min single CPU
- Individual logarithms, a few min, single CPU

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- SGE: from 50 M eq. in 2.1 M var.
 ⇒ 666 K eq./var.
- Lanczos 27 hours on 128 CPUs
- Completion, 10 min single CPU
- Individual logarithms, a few min, single CPU

Total 152 CPU.days

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Going to 6×23 and 6×24

Antoine Joux HPC in Cryptanalysis

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Going to 6×23 and 6×24

• 2a: Structured Gaussian Elimination

- 6×24 : Not enough memory. Need to work on disk
- 6×25 : Too slow. Need to multi-thread
- Corruption of equations on disk:
 - \Rightarrow Add a verification of relations

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Going to 6×23 and 6×24

2a: Structured Gaussian Elimination

- 6×24 : Not enough memory. Need to work on disk
- 6×25 : Too slow. Need to multi-thread
- Corruption of equations on disk:
 ⇒ Add a verification of relations
- 2b: Lanczos: Getting slow
 - Time limit on jobs: need to save/restart
 - Need to supervise the process

Computation performed on GENCI's Curie¹ (PRACE Projects 2010PA0421 and 2011RA0387)

¹Same computer used for all subsequent computations (D > (E > (E > (E > (E > (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C < (C <

Computation performed on GENCI's Curie ¹ (PRACE Projects 2010PA0421 and 2011RA0387)

• Sieving: About 3.5 hour on 1024 CPUs

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Computation performed on GENCI's Curie¹ (PRACE Projects 2010PA0421 and 2011RA0387)

- Sieving: About 3.5 hour on 1024 CPUs
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Computation performed on GENCI's Curie ¹ (PRACE Projects 2010PA0421 and 2011RA0387)

- Sieving: About 3.5 hour on 1024 CPUs
- SGE: Not enough memory

 \Rightarrow Rewrite to work on disk. Becomes too slow: need to multi-thread

- New SGE: from 870 Meq. in 4.2 M var.
 - \Rightarrow 1 M. eq./var. Using a few hours on 32 CPUs.

¹Same computer used for all subsequent computations **B** + (**E** + (**E** +) **E** - **S** < **C**

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- Corruption of some equations on disk:
 - \Rightarrow Add a verification of relations
- Lanczos 73 hours on 64 CPUs

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- Corruption of some equations on disk:
 - \Rightarrow Add a verification of relations
- Lanczos 73 hours on 64 CPUs
- Completion, 17.5 hours single CPU

¹Same computer used for all subsequent computations 🐵 🛛 🖘 🖘 😨 🔊 ର୍ବ

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- Completion, 17.5 hours single CPU
- Individual logarithms, a few min, single CPU

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 - \Rightarrow 1 M. eq./var. Using a few hours on 32 CPUs.
- Corruption of some equations on disk:
 - \Rightarrow Add a verification of relations
- Lanczos 73 hours on 64 CPUs
- Completion, 17.5 hours single CPU
- Individual logarithms, a few min, single CPU

Total 350 CPU.days

Antoine Joux HPC in Cryptanalysis

• Sieving: About 15 hours on 1024 CPUs

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- Sieving: About 15 hours on 1024 CPUs
- New SGE: from 3.5 Geq. in 8.4 M var.
 - \Rightarrow 1.7 M. eq./var. Using a few hours on 32 CPUs.

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- Sieving: About 15 hours on 1024 CPUs
- New SGE: from 3.5 Geq. in 8.4 M var.
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- Sieving: About 15 hours on 1024 CPUs
- New SGE: from 3.5 Geq. in 8.4 M var.
 ⇒ 1.7 M. eq./var. Using a few hours on 32 CPUs.
- Corruption of some equations on disk:
 ⇒ Add a verification of relations
- Lanczos 11 days on 64 CPUs

- Sieving: About 15 hours on 1024 CPUs
- New SGE: from 3.5 Geq. in 8.4 M var.
 ⇒ 1.7 M. eq./var. Using a few hours on 32 CPUs.
- Corruption of some equations on disk:
 ⇒ Add a verification of relations
- Lanczos 11 days on 64 CPUs
- Completion, 13 hours single CPU

- Sieving: About 15 hours on 1024 CPUs
- New SGE: from 3.5 Geq. in 8.4 M var.
 ⇒ 1.7 M. eq./var. Using a few hours on 32 CPUs.
- Corruption of some equations on disk:
 ⇒ Add a verification of relations
- Lanczos 11 days on 64 CPUs
- Completion, 13 hours single CPU
- Individual logarithms, a few min, single CPU

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- New SGE: from 3.5 Geq. in 8.4 M var.
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- Corruption of some equations on disk:
 ⇒ Add a verification of relations
- Lanczos 11 days on 64 CPUs
- Completion, 13 hours single CPU
- Individual logarithms, a few min, single CPU

• Total 1350 CPU.days \approx 3.7 CPU.years

Going to 6×25

Antoine Joux HPC in Cryptanalysis



- Lanczos: Getting slow
 - Time limit on jobs: need to automate save/restart
 - Need to supervise the process

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Going to 6×25

- Lanczos: Getting slow
 - Time limit on jobs: need to automate save/restart
 - Need to supervise the process
- Completion of logarithms
 - Related to SGE: Becoming harder
 - Occasional corruption of logarithms on disk !
 - \Rightarrow Add a correction step to remove false logs

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Antoine Joux HPC in Cryptanalysis

Sieving: About 62 hours on 1024 CPUs

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- Sieving: About 62 hours on 1024 CPUs
- New SGE: from 14 Geq. in 16.8 M var.
 ⇒ 3.1 M. eq. Using a few runs on 32 CPUs. Total 25.5h on 32 CPUs.

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- Sieving: About 62 hours on 1024 CPUs
- New SGE: from 14 Geq. in 16.8 M var.
 ⇒ 3.1 M. eq. Using a few runs on 32 CPUs. Total 25.5h on 32 CPUs.
- Lanczos 28.5 days on 64 CPUs

- Sieving: About 62 hours on 1024 CPUs
- New SGE: from 14 Geq. in 16.8 M var.
 ⇒ 3.1 M. eq. Using a few runs on 32 CPUs. Total 25.5h on 32 CPUs.
- Lanczos 28.5 days on 64 CPUs
- Completion becoming too slow: multi-threaded version
 ⇒ 12 hours on 32 CPUs

- Sieving: About 62 hours on 1024 CPUs
- New SGE: from 14 Geq. in 16.8 M var.
 ⇒ 3.1 M. eq. Using a few runs on 32 CPUs. Total 25.5h on 32 CPUs.
- Lanczos 28.5 days on 64 CPUs
- Completion becoming too slow: multi-threaded version
 ⇒ 12 hours on 32 CPUs
- Individual logarithms, improved code: 1 min, single CPU

- Sieving: About 62 hours on 1024 CPUs
- New SGE: from 14 Geq. in 16.8 M var.
 ⇒ 3.1 M. eq. Using a few runs on 32 CPUs. Total 25.5h on 32 CPUs.
- Lanczos 28.5 days on 64 CPUs
- Completion becoming too slow: multi-threaded version
 ⇒ 12 hours on 32 CPUs
- Individual logarithms, improved code: 1 min, single CPU

• Total 4470 CPU.days \approx 12 CPU.years

Antoine Joux HPC in Cryptanalysis

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- Theory:
 - Phase 1: Sieving
 - Phase 2: Linear algebra
 - Phase 3: Individual logarithms

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 - 1b: Verification of relations (fast)

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• New view confirmed by 6×25

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- Sieving and verification OK 8192 CPUs for 24 hours
- SGE OK: From 40 Geq in 33.5 M var
 - \Rightarrow 5.9 M eq. A few 10h runs on 32 CPUs
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Back to the drawing board

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 - Recompute iterated matrix multiplications in parallel to obtain solution
- Need to scale up the approach

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- Total CPU time \approx 50 500 hours, 2100 CPU.days
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- Second Matrix Vector Phase: \approx 15h30 on 1024 cores
- Total CPU time \approx 50 500 hours, 2100 CPU.days
- Real time (without waits) \approx 2.5 days
- New total real time including Sieving: \approx 5 days \approx 14 CPU.years

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• First Matrix Vector Phase: \approx 125 h on 1024 cores 32 independent sequences

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- Started March 28th

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• Still running ... (Curie very busy these days)

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Questions ?

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